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“ ”

: 5.2

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• -

, 2018 .

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18.06.2018 .

15.10.2018 .

o 15:00

-221 / 26.06.2018 .

1. . -

2. . -

3. . -

4. . .

5. . .

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1. . .

2. . -

1, 1332 .

”

“

: . .

: ”

: 30

“

I.

[1,2,3,4,5,6]

[13]

1.

()

2.

()

3.

4.

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5.

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6.

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7.

— ”

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1-

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2-

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3-

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9 19.12.2017 . “ ” “ .

. .

191 , 51 .

, 3 , ,

, 115 , 92 5

, 47 , 4 6 101 .

, , .

II.

1.

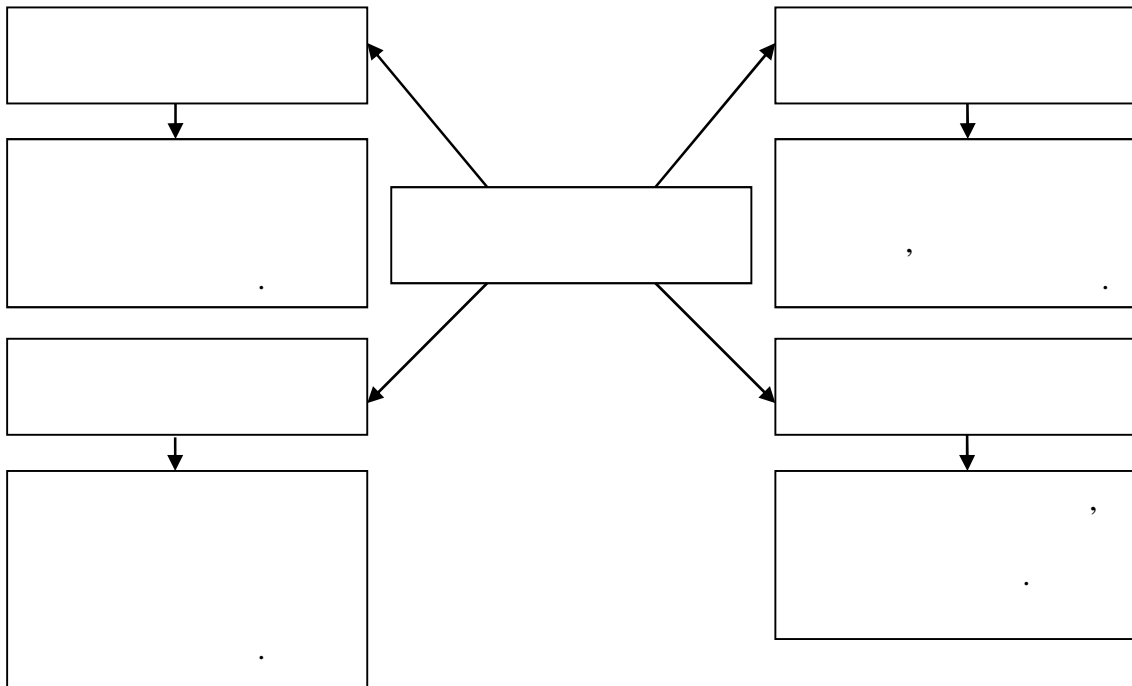
(

,

)

[4, 63, 76, 79, 80]

2.35

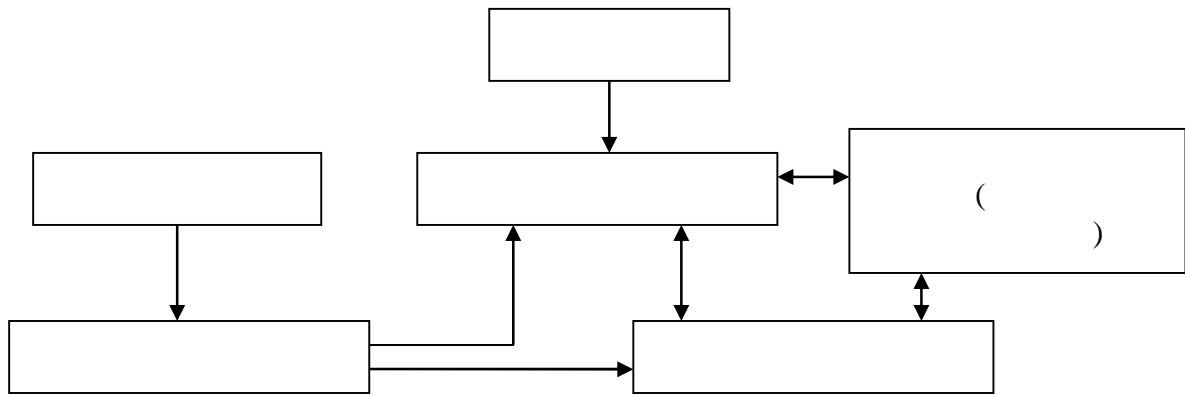


. 2.35.



„ 1”

2.36.



. 2.36.

1.

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-
-
-
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-
-

SPI, I2C, UART
 RS232, One Wire, RS485, Ethernet GPRS

-
-
-
-
-

1)

() .

2)

3)

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1)

2)

3)

4)

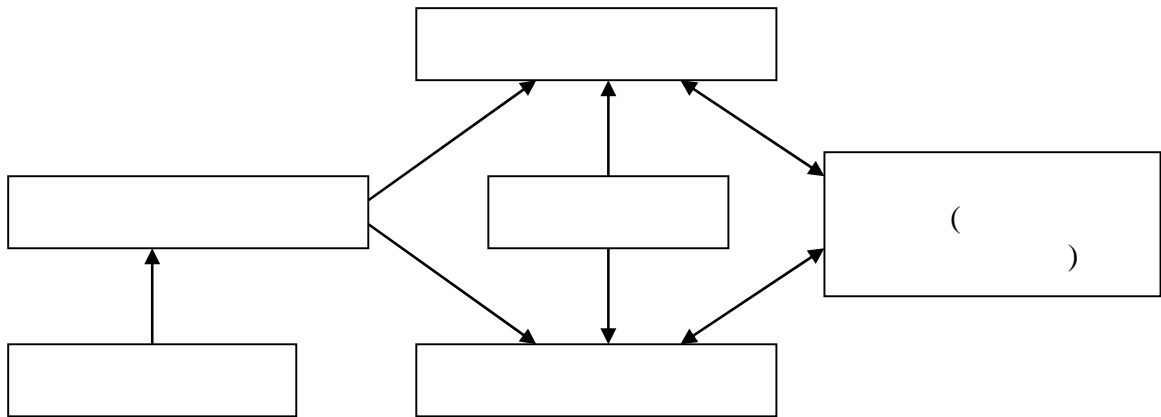
5)

6)

„ 2” ,

2.37,

() .



.2.37.

2.

2 :



1)

2)

3)

:

1)

2)

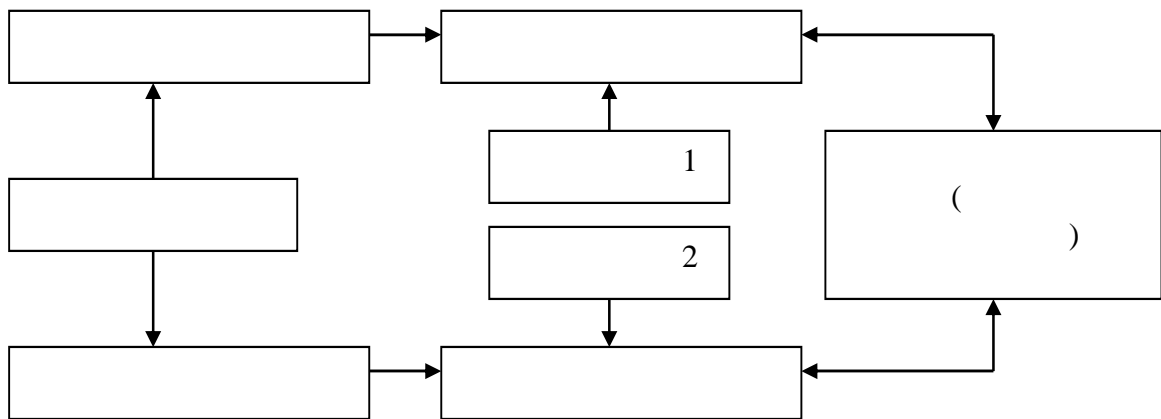
3)

4)

5)

Ethernet

„Wireless“.



. 2.38.

3.

„ 3” :



1)

2)

3)

4)

:

1)

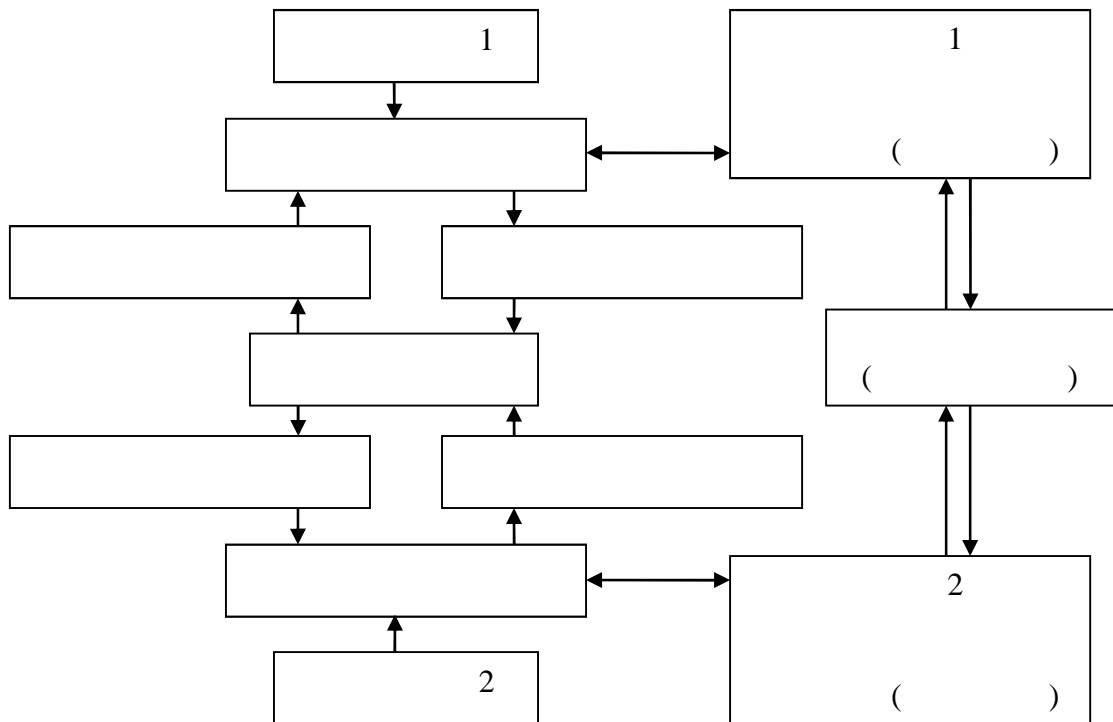
2)

3)

4)

4,

2.39,



. 2.39.

4.

(

),

,

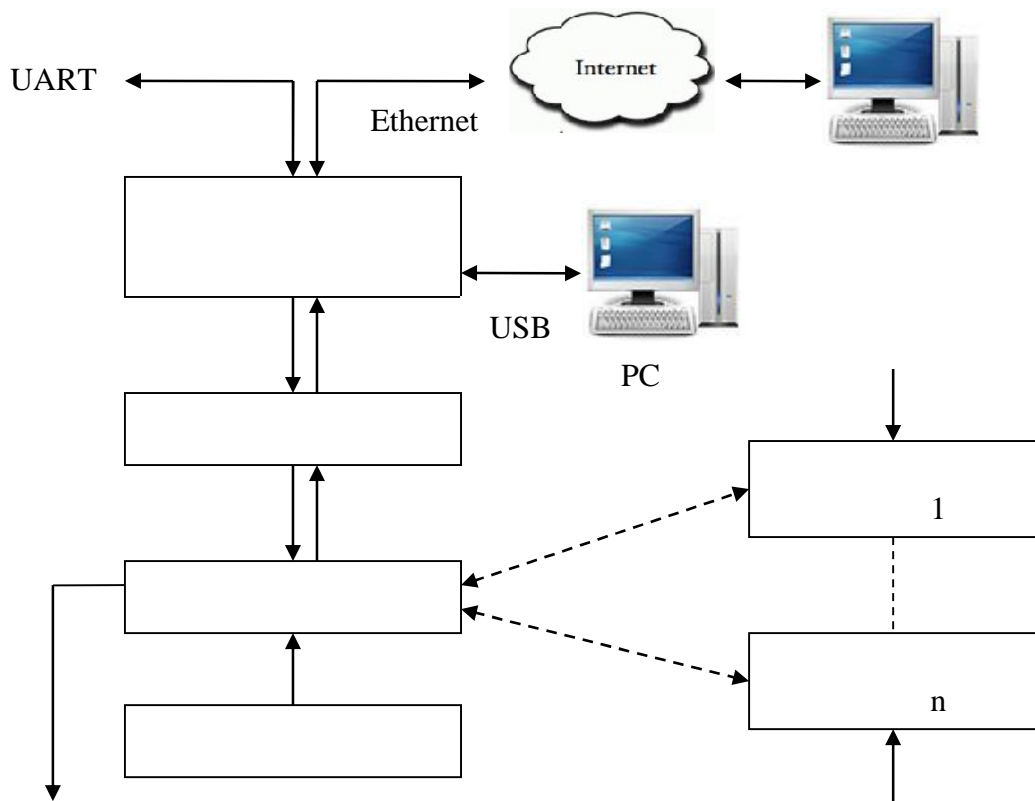
,

- -
 -
- 1)
- 2)
- 3)
- 4)
- 5)
- 6)
- 1) ().
- 2) ,
- 3) .
-

2.

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 ,
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 ,
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3.1.



. 3.1.

1)

2)

3)

1)

2)

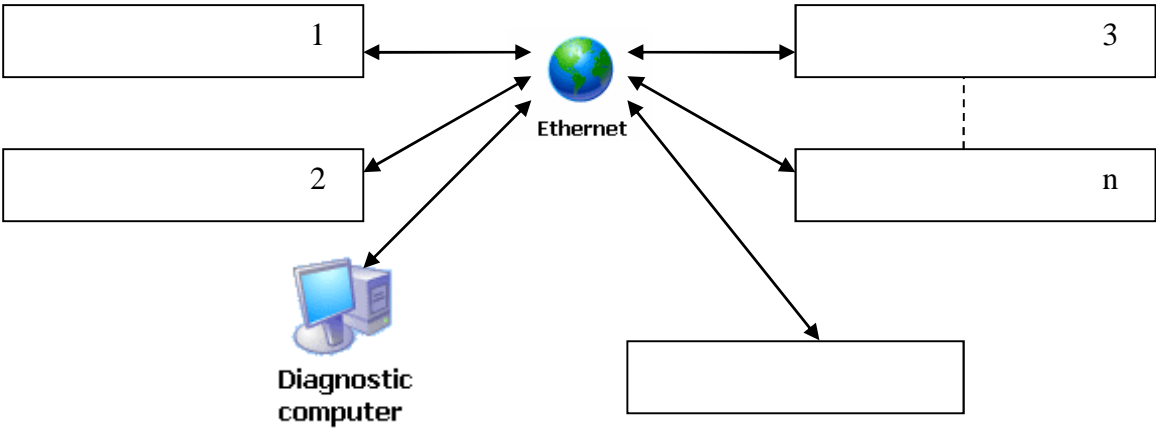
SIM

-
-
- 1-
- 2-
- 3-
-
-
-
-

3.4

Ethernet

RTC



. 3.4.

Ethernet . [82]

[85]

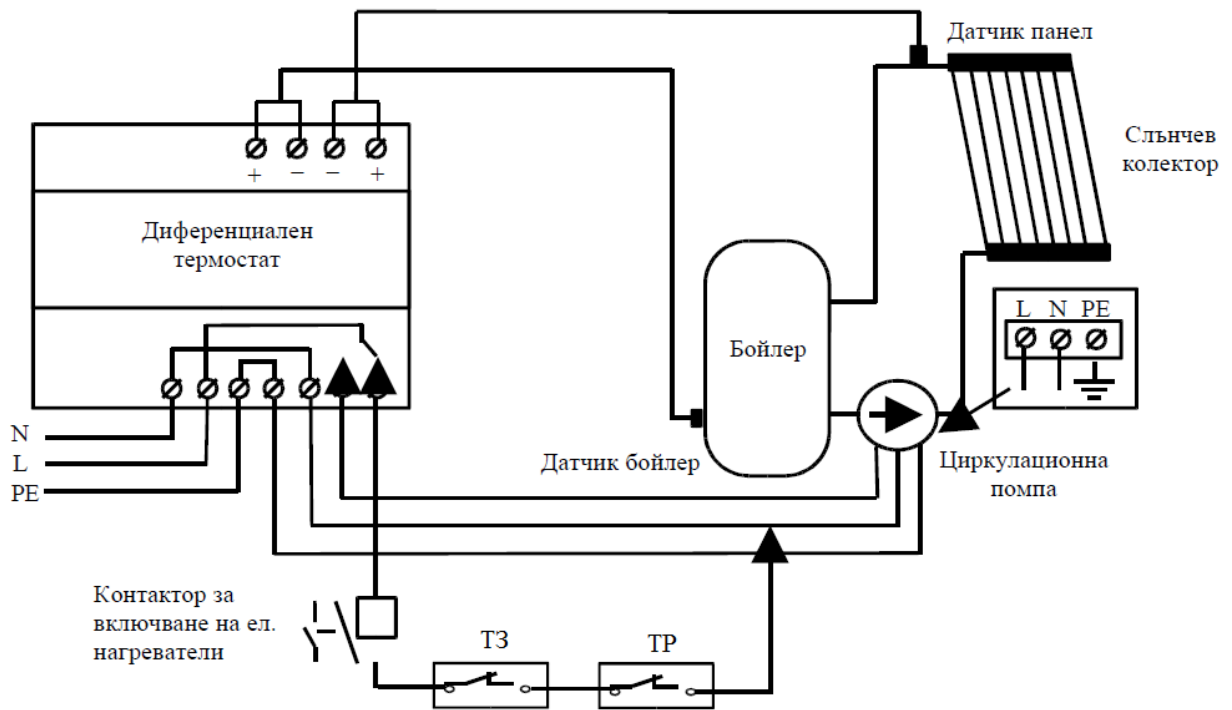
()

LM335

LM324.

WS7107.

3.8



. 3.8.

. [85]

” “ ” “

()



;

;

;

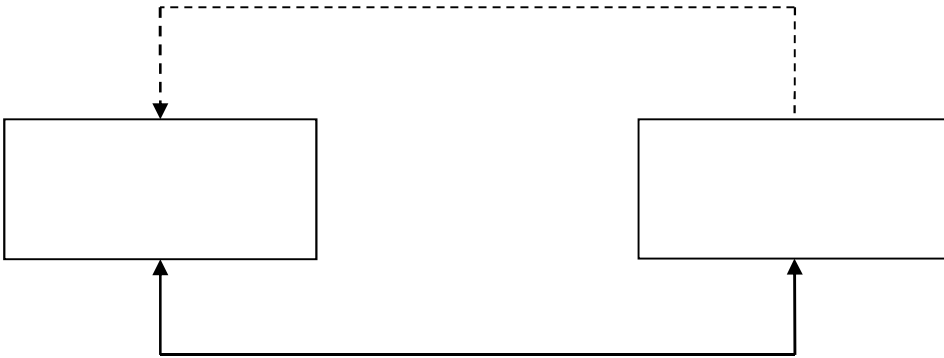
;

;

;

3.9.

I1, I2, P, Lux



I1, I2, P, Lux, Time, Port - K1, K2

. 3.9.

1.

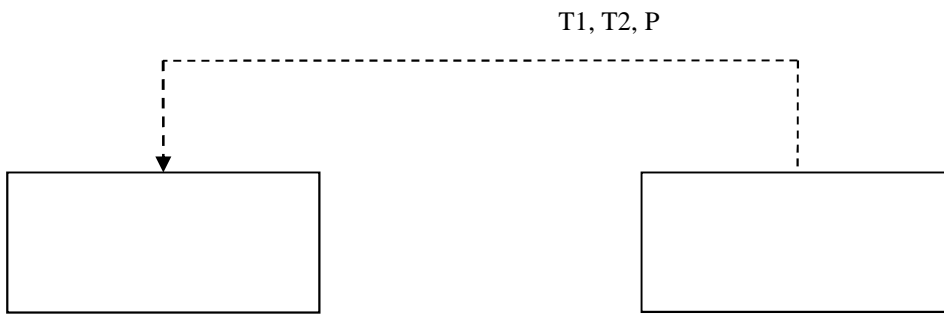
1.

2.

UART

- T1;
- 2;
- ;
- T1-T2;

3.10.

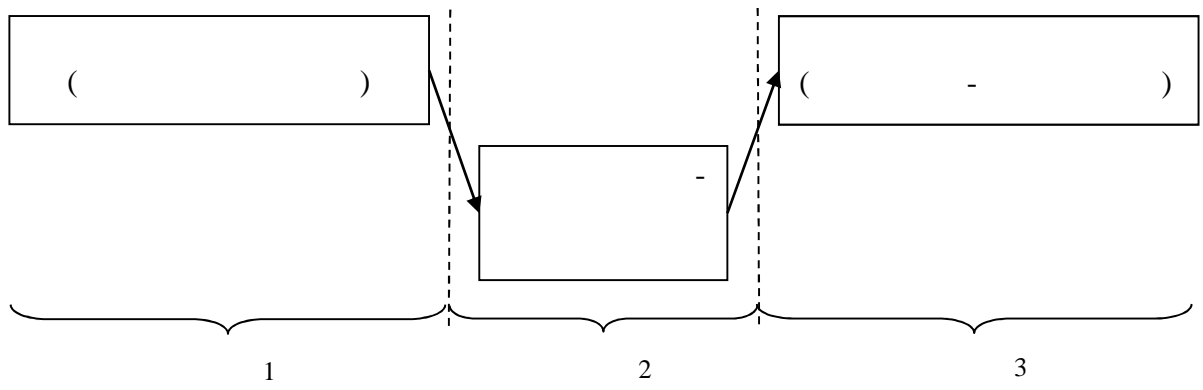


. 3.10.

2.

3.

4.1.

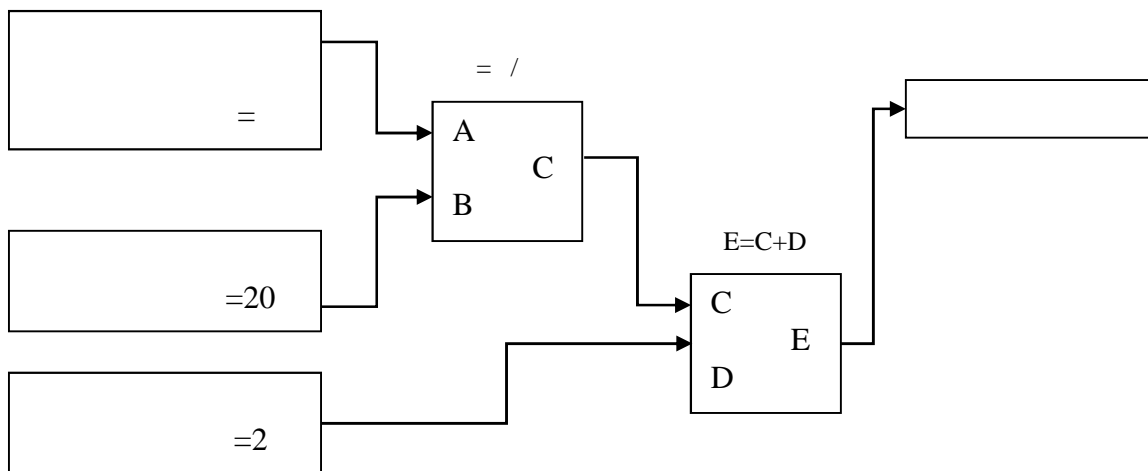


.4.1.

1. / ;
2. ;
3. /

, й

4.2:

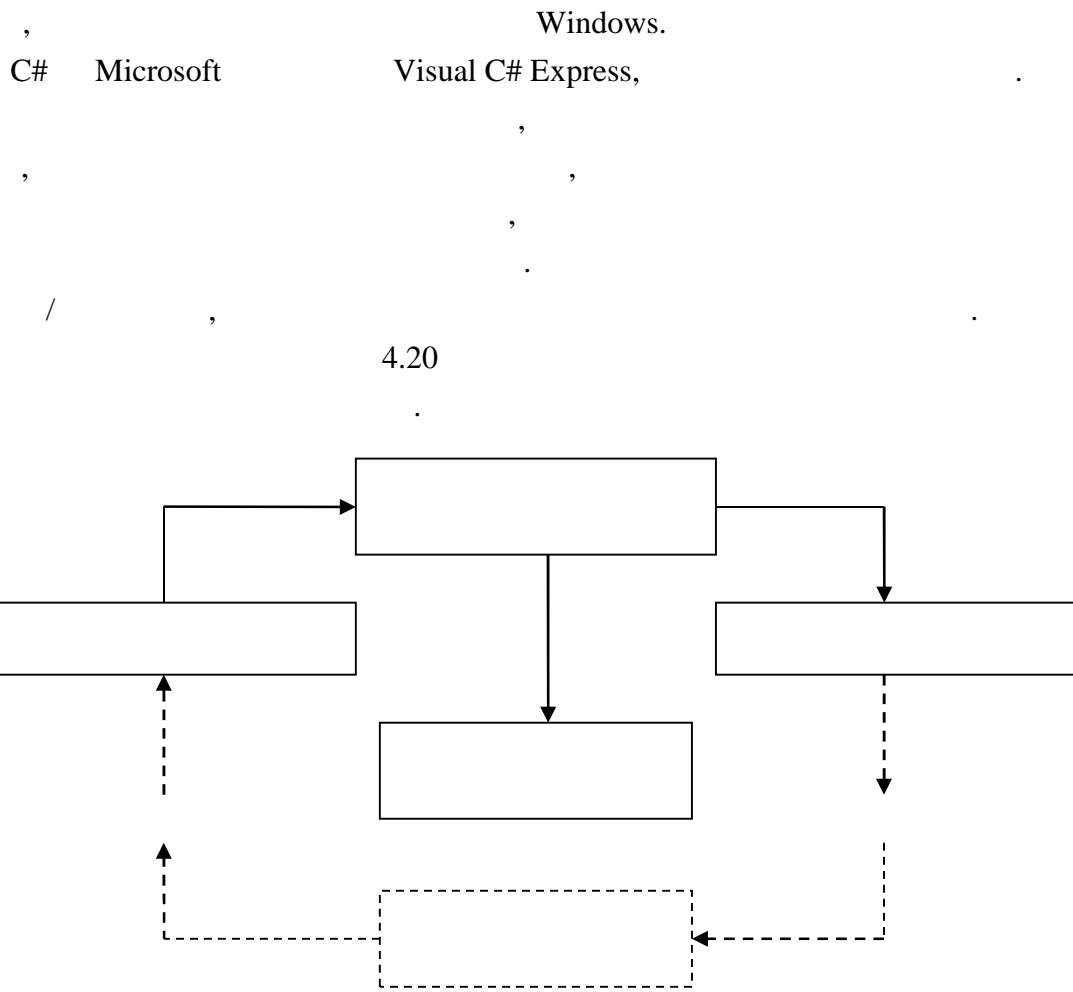


.4.2.

A/B

, -
,
A/B.

C+D



.4.20.

(L) - (MCM - laminate).

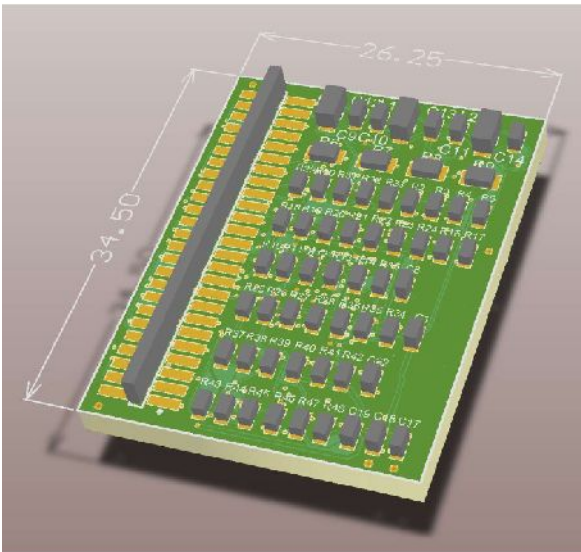
$$f_T = 50\text{MHz},$$

10

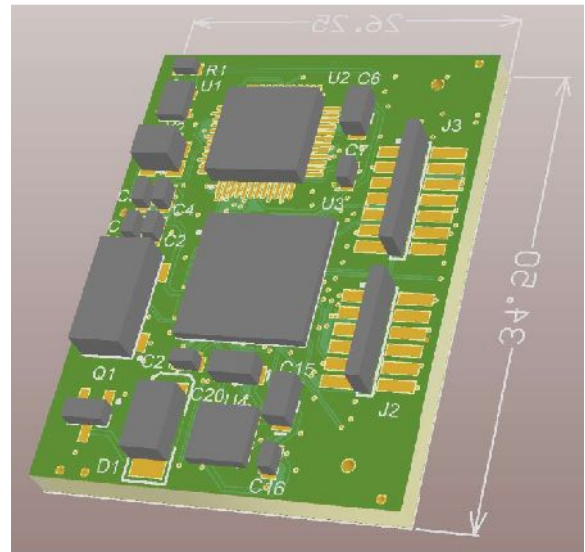
$$f_{T10}[\text{MHz}] = f_T \cdot 10 = 50\text{MHz} \cdot 10 = 500\text{MHz} \quad (\text{Error! No text of specified style in document..1})$$

4.34 4.35

L1 L8

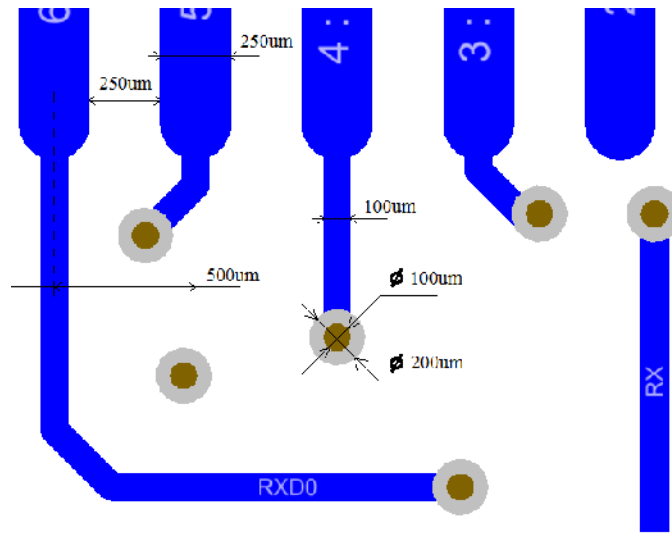


.4.34. — L1.



.4.35. — L8.

4.39
QFP48.



. 4.39.

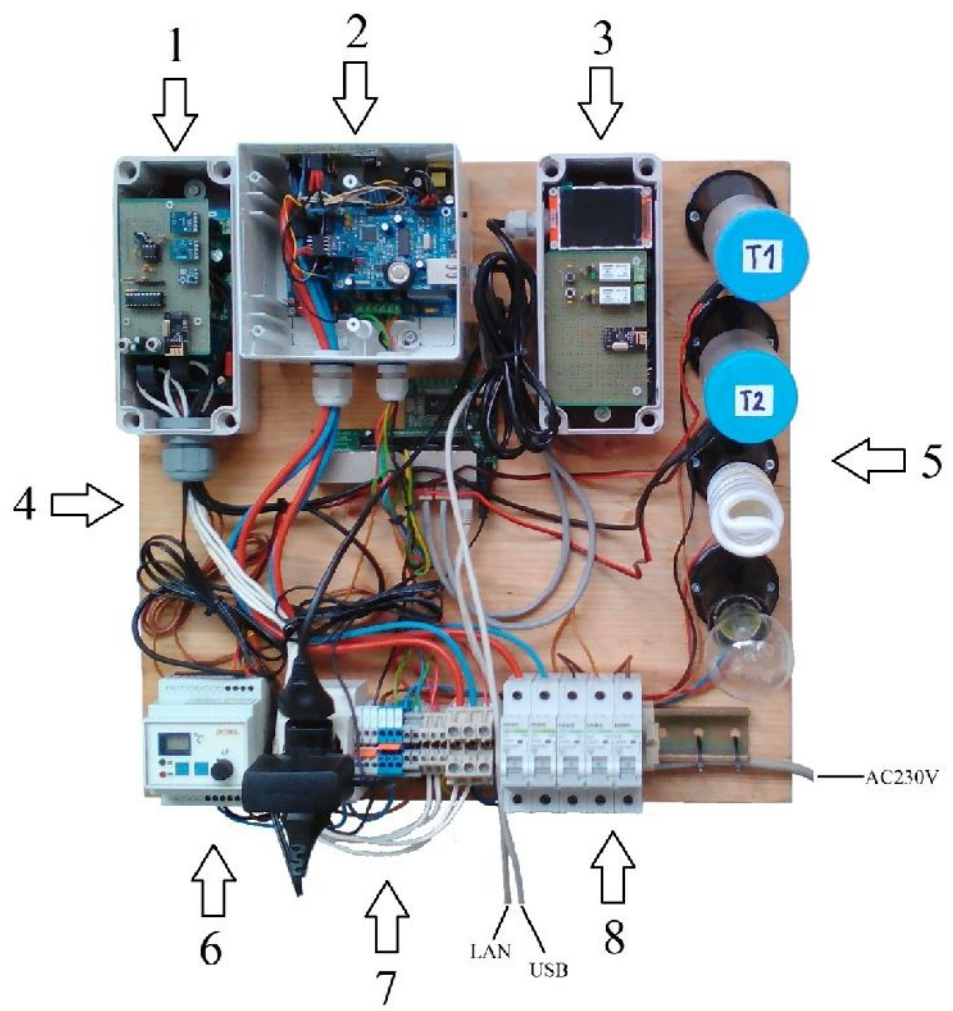
bottom layer (L8).

25µm

-
- ϵ_{eff} ;
- Z_0 ;
- T_{PD} ;
- f_C ;
- n- f_{Tn} ;
- λ_n ;
- λ_g ;
- α_d ;
- :
- $w = 100 \mu m$;
- $h = 320 \mu m$;
- $k = 250 \mu m$;
- $d = 35 \mu m$;
- FR4 - $\epsilon_r \approx 4,3$;
- FR4 - $tg \delta_\epsilon \approx 0,02$;

-
-
-

4.41



. 4.41.

- Windows7
- UAC UACeditor;
- ; [3]
- 1; [1]
-
- 2; [2]
- - „Hub”; [4]
- ;
- [7]
- -
- T1 T2,

I1 I2.

I1

I2

; [5]

; [8]

DT-3 (); [6]

„UTP CAT5E” RJ45

USB ;

USB LAN

AC230V

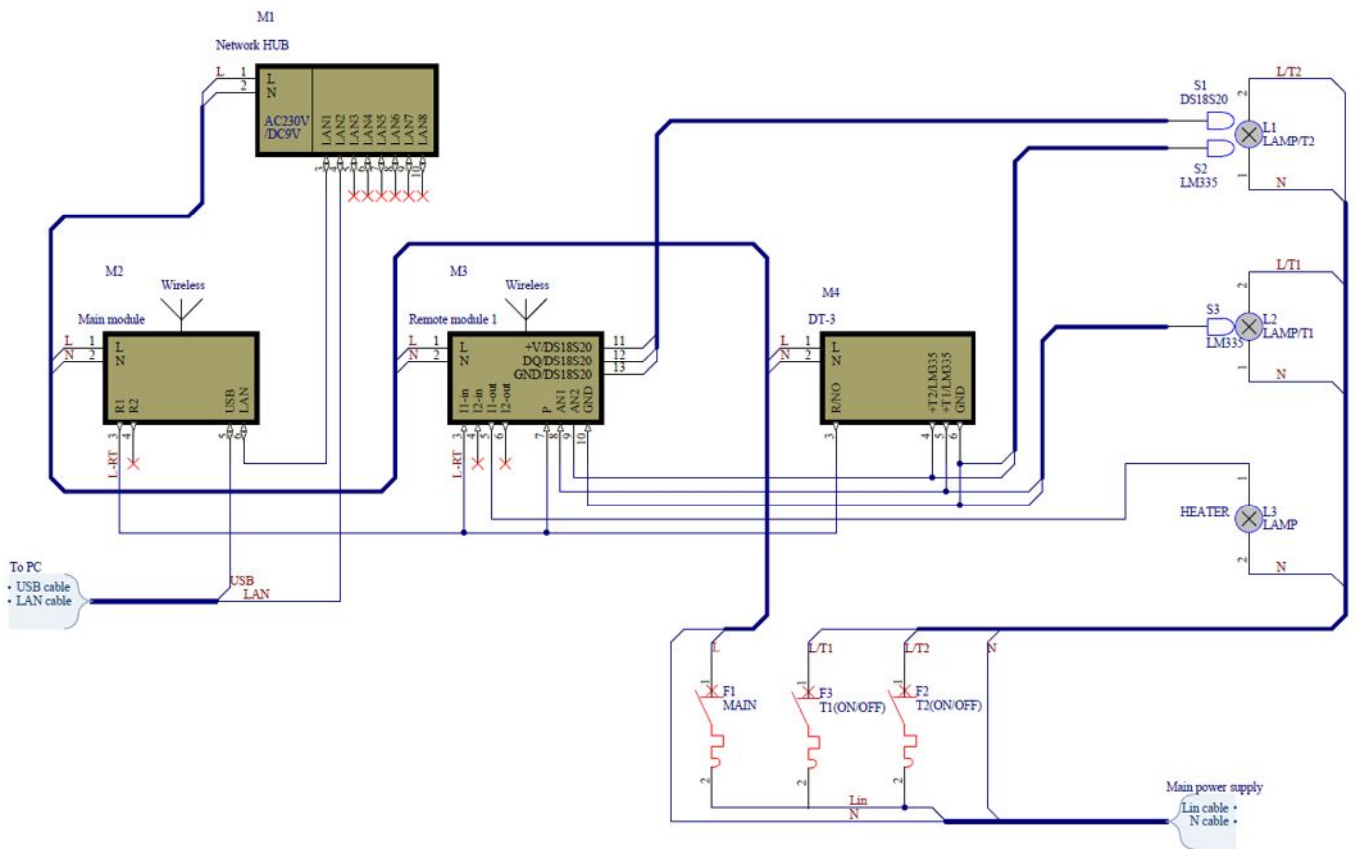
3 1,5mm².

DT-3.

- DT-3.

LM335

8.42



. 8.42.

DT-3.

1.



2.



1 2

PL15W,

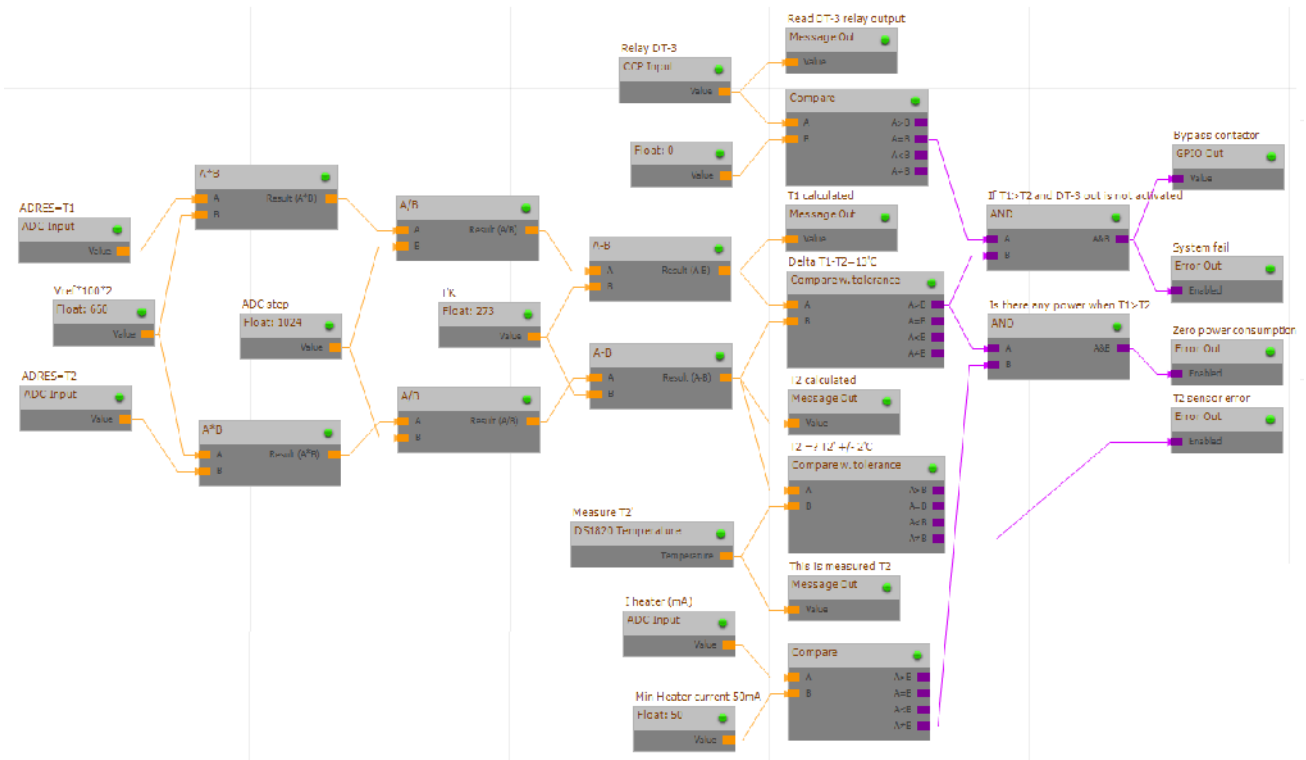
F2 F3.

2, 3

(UACeditor)

DT-3.

8.43



. 8.43.

DT-3.

4.48

1



.4.48.

DT-3.

4

_____ :
✓

✓

✓

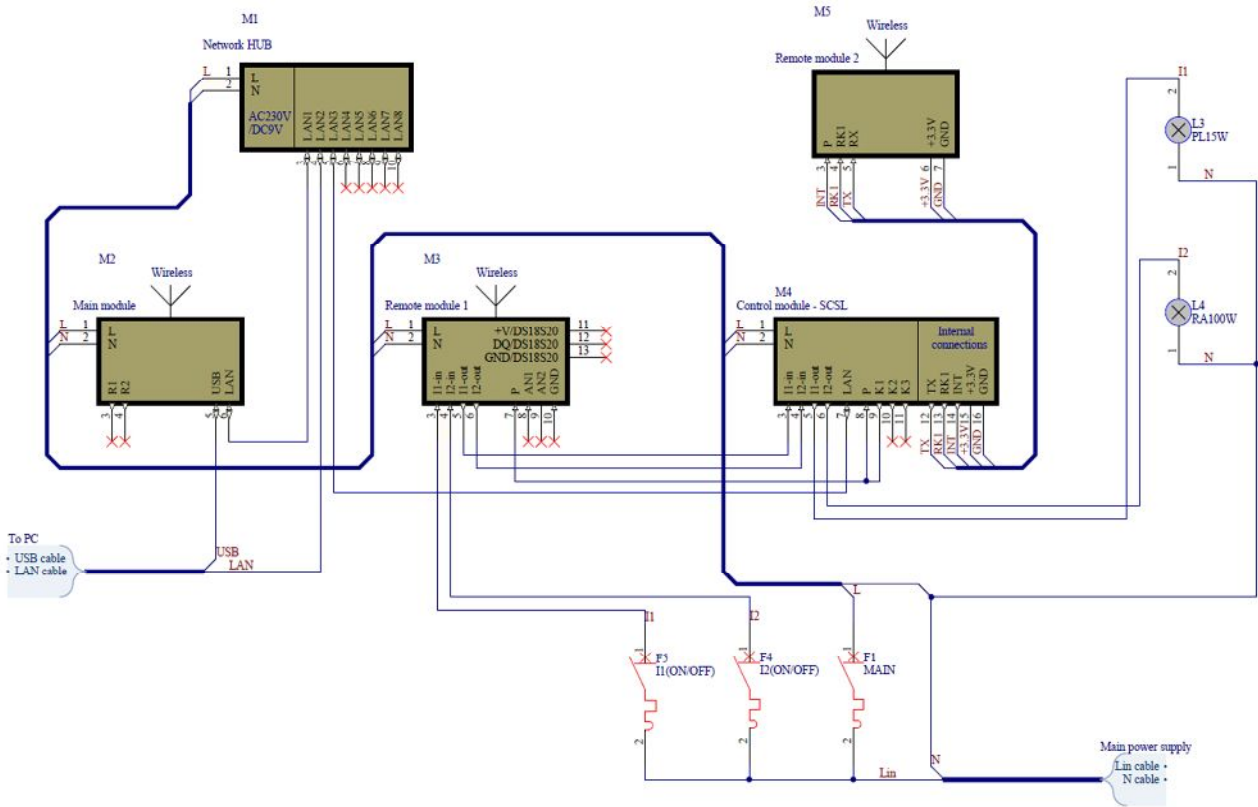
✓

✓

✓

_____ :

T2



. 8.45.

3. — UART :

➤ I1 I2;

➤ (Lux);

➤ ;

➤ — / ;

➤ — : : ;

4. :

➤ I1 I2;

➤ (Lux);

➤ ;

➤ RK1 1;

F3 F4

I1 I2

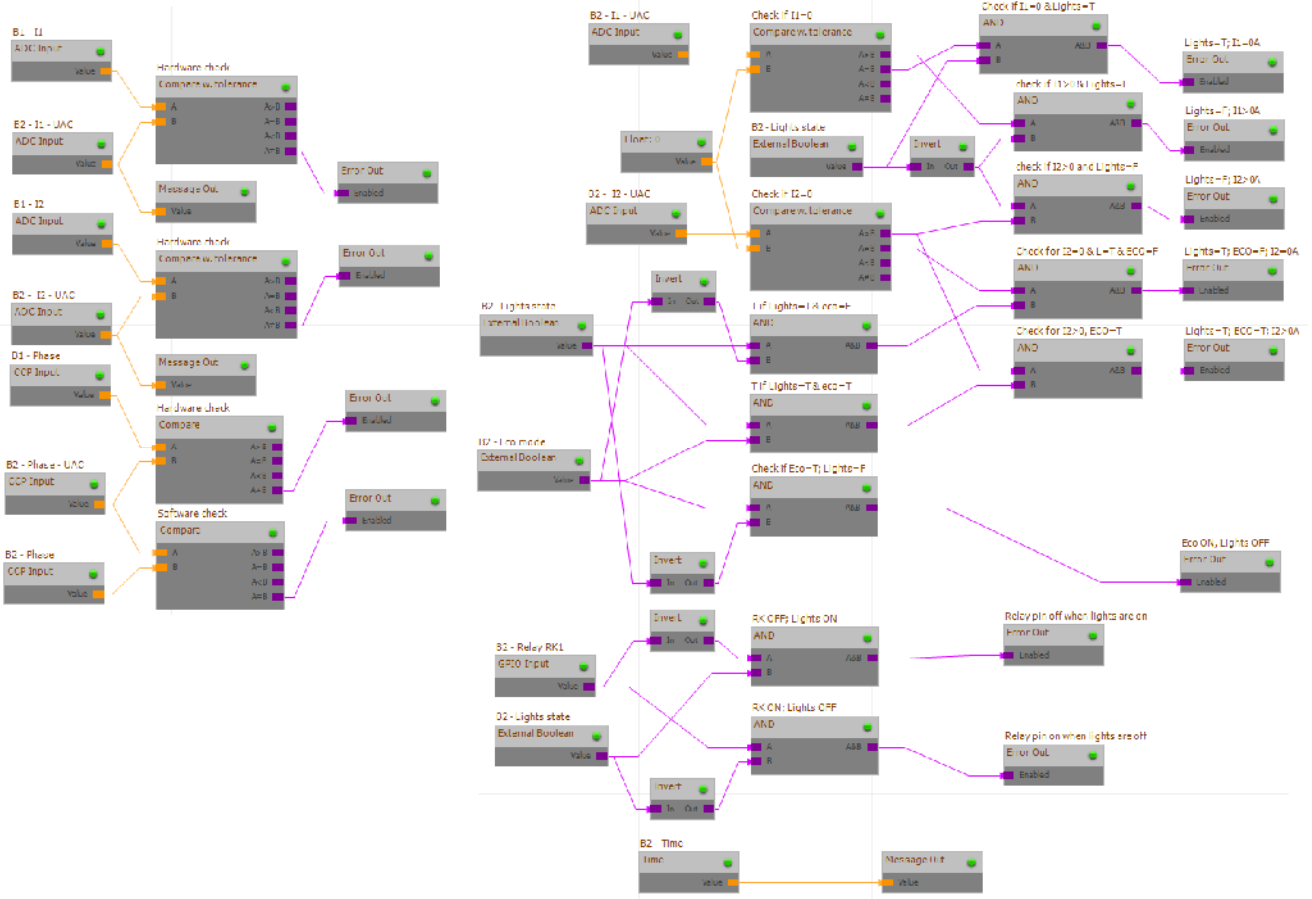
2, 3 5

27

(UAC)

(UACeditor).

8.46



. 8.46.

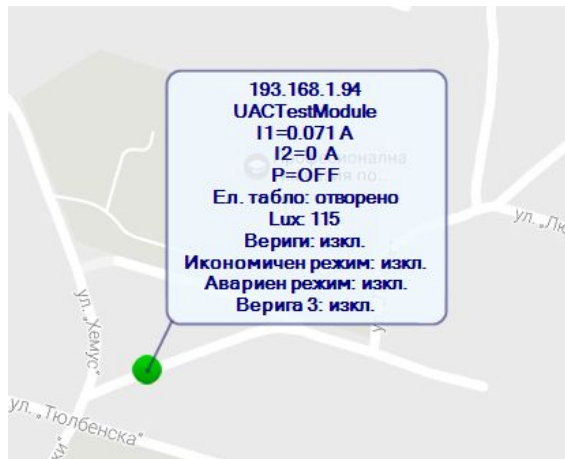
4.49

I1 I2

10mV

50Hz

5mS
50mV.



. 8.49.

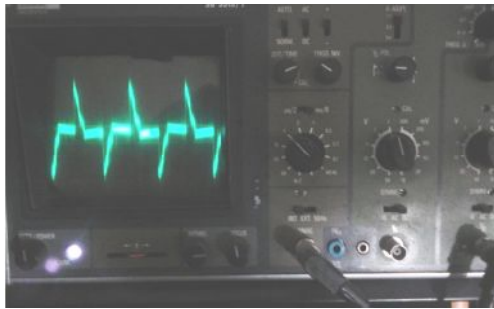
a

- UAC.

4.50

II.

15W.



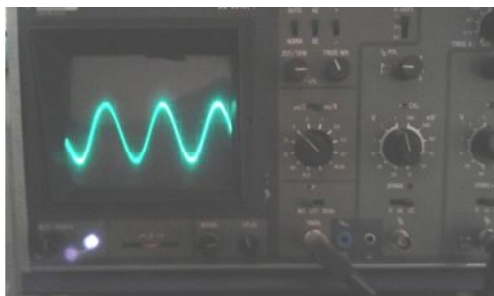
. 4.50.

I1

4.51

I2

100W.



. 4.51.

I2

5mS,

20mV.

5

_____ :
✓

✓

✓

✓

✓

;

1

, II I2;

1

12W

15W.

U=231V;

1.

2.

1.

A

”

” –

9 19.12.2017 .

”

”

2.

3.

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SUMMARY

Specialized multichip modules used for monitoring and control of a process in electronic and automated systems

Electronic devices who are used for monitoring and control of other electronic systems or processes are the backbone of complex management systems in all areas of human development. The electronic system can be varying degrees of complexity and responsibility as an application, but nevertheless the problem of its work is always up to date.

The connection between the multi-chip module and the current trends is expressed in the constant pursuit of miniaturization and compactness of the devices. Modular design enhanced functionality and enables easy reconfiguration.

The purpose of this dissertation thesis is to prepare a specialized module for monitoring and control of other electronic systems, processes or parameters. The main purpose is to develop a method for block programming. The algorithm of operation can be done by realizing interconnections between individual blocks and setting of working parameters by the user. The proposed model is tested by monitoring selected electronic systems according to the implemented algorithm. The method for block programming is carried out by a specially designed graphic editor. The characteristic being that the data processing is done in three phases.

In order to validate the proposed software model and the developed software and hardware, algorithms for monitoring and control of real-world systems in practice have been implemented. The results of the dissertation work give a finished look to the designed experimental model, realizing an object-oriented system for monitoring, control and management of different systems or processes.

As a quick conclusion it can be pointed out that for the purpose of monitoring and control it is necessary to know the logic of operation and the schematics of the monitored system. The approach is used first with theoretical analysis and then it is done in practical testing. The final component is to adjust, then we have the desired result.